SYSTEM FOR MATCHING RISE AND FALL TIMES OF DRIVE SIGNALS IN A DIGITAL TO ANALOG CONVERTER

ABSTRACT OF THE DISCLOSURE

A system (e.g., a digital-to-analog converter (DAC)) includes a digital section and an analog section. The digital section has a driver portion that generates drive signals based on received respective digital input signals. The drive signals are received at respective switches in the analog section. The driver portion includes logic gates that are used to generate the drive signals, such that a rise and fall time of complementary pairs of drive signals are substantially equal. The driver portion can optionally include an acceleration system to accelerate the rise and fall times of the drive signals. The switches generate respective analog signals from the drive signals.

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